

“The Incredible Shrinking Faraday Cage”

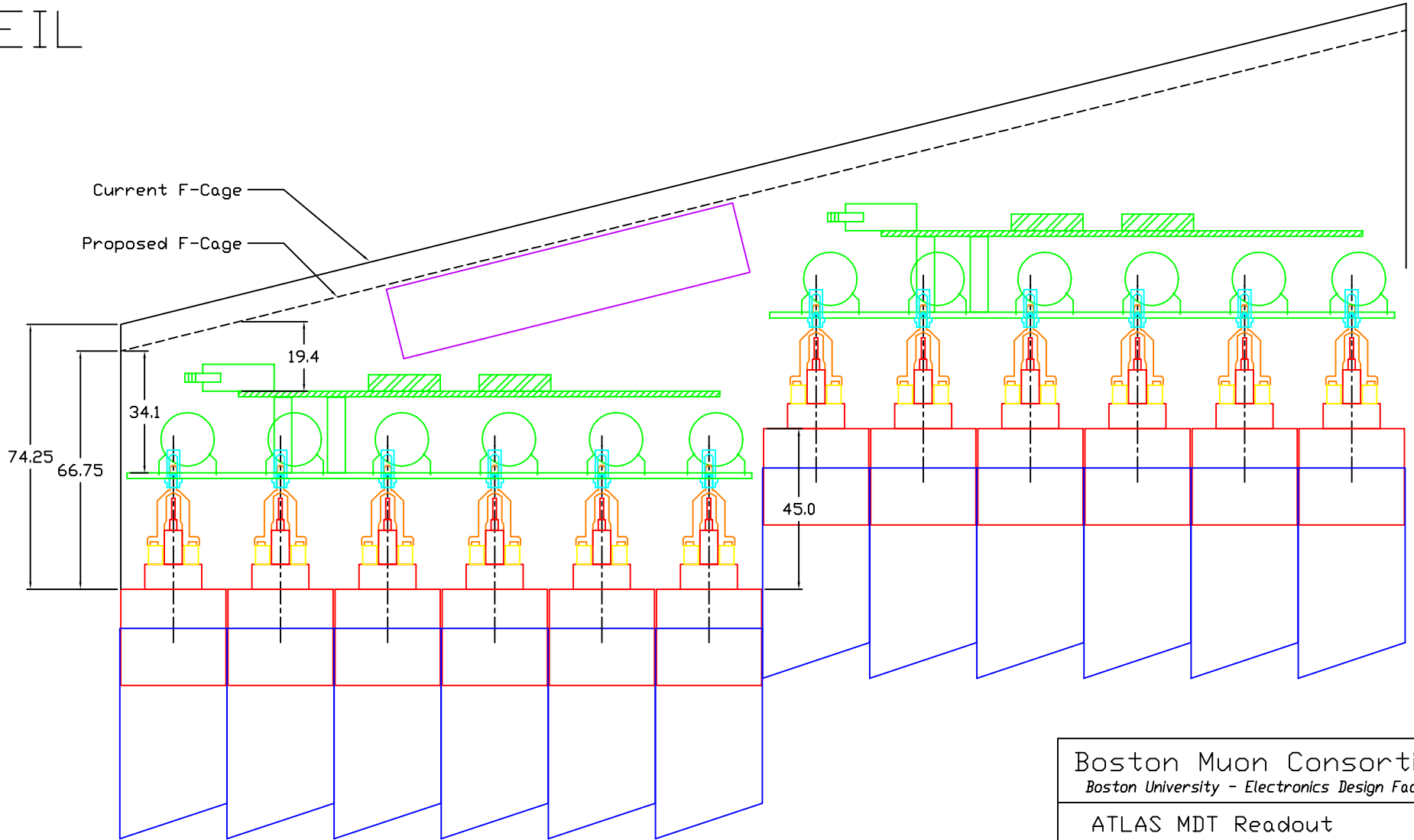
by E. Hazen

| | | |
|-----------|-----------|-----------------|
| Drawings: | EIL | (4-layer, 14°) |
| | EIS | (4-layer, 8.5°) |
| | EM/EO/EEL | (3-layer, 14°) |
| | EM/EO/EES | (3-layer, 8.5°) |
| | BI | (4-layer) |
| | BM/BO | (3-layer) |

Notes:

1. If we accept the size of the faraday cage for the barrel chambers (18.7mm from mezzanine card to f-cage outside) then we are compelled to accept the latest changes to the endcap, I think.
2. Where does the patch panel go in the BI chambers?

EIL



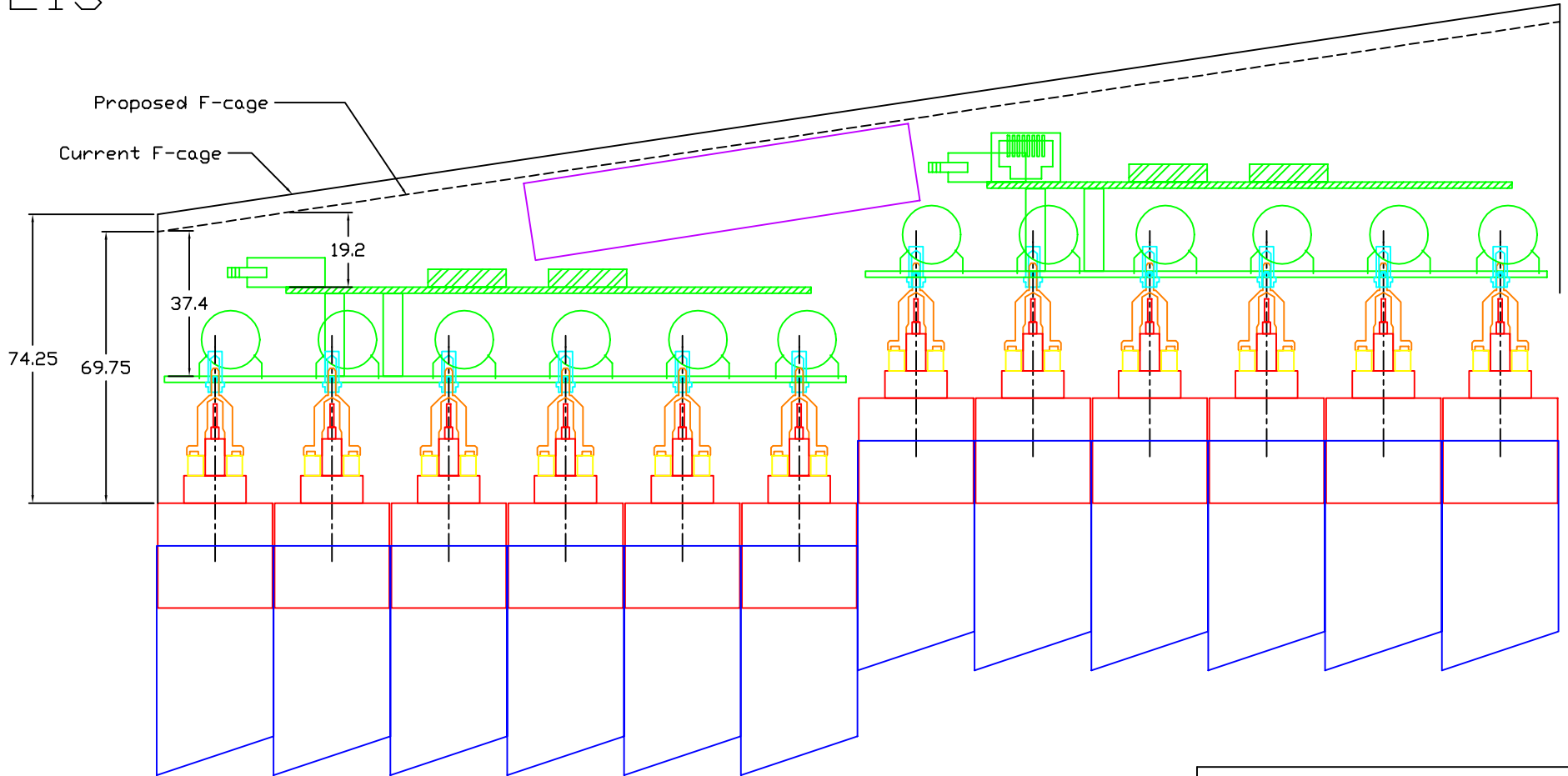
NOTE: FOR DISCUSSION
Proposed F-Cage dimensions from JW 1999-01-27

Boston Muon Consortium
Boston University - Electronics Design Facility

ATLAS MDT Readout
EIL Chamber Type
PCB Stack Reference Dwg

| | |
|----------------------------|-------------------------|
| SCALE: <i>DO NOT SCALE</i> | Revision: <i>XXX</i> |
| Dwg: <i>E. Hazen</i> | Date: <i>1999-01-28</i> |

EIS



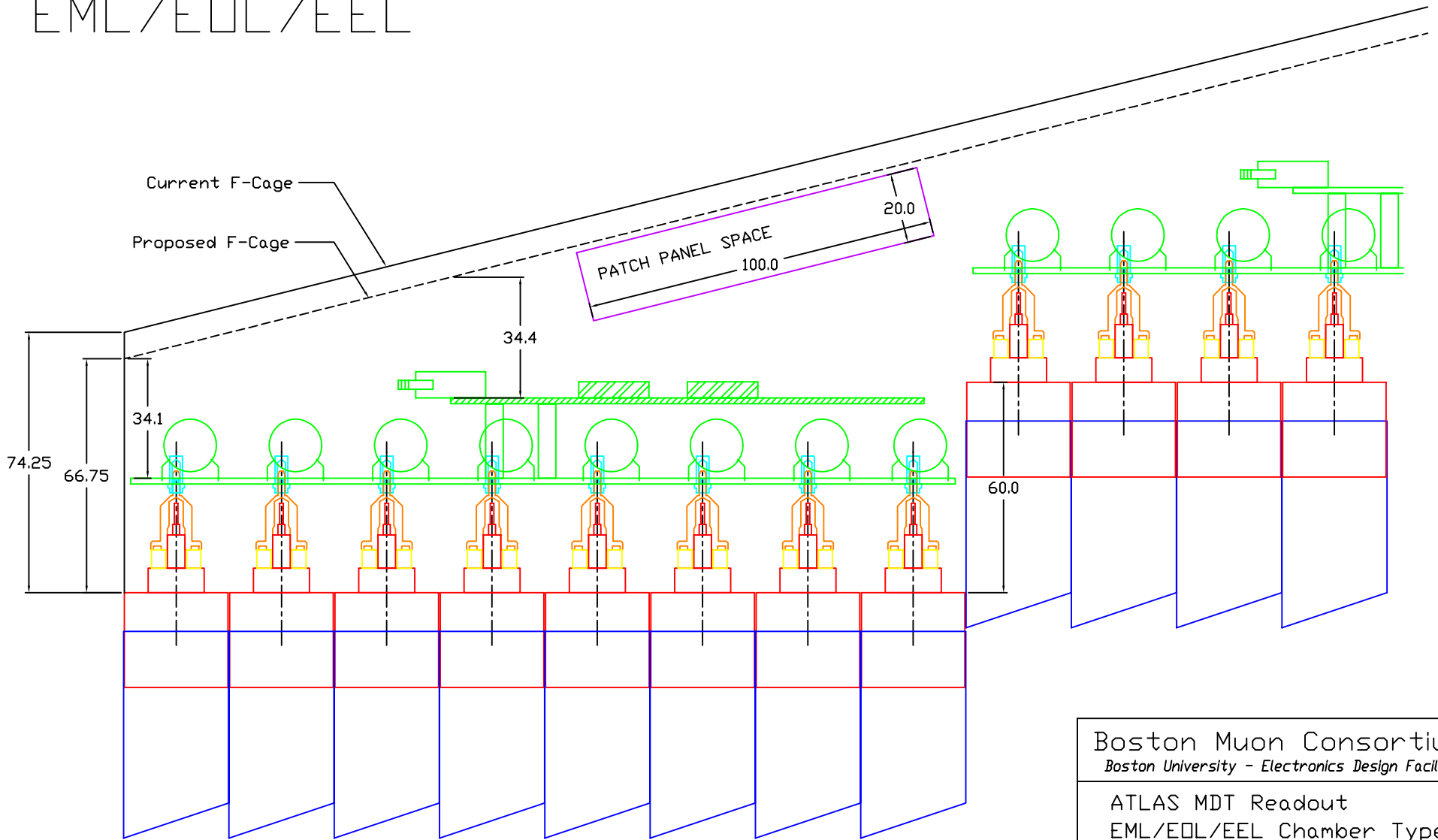
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EML/EOL/EEL



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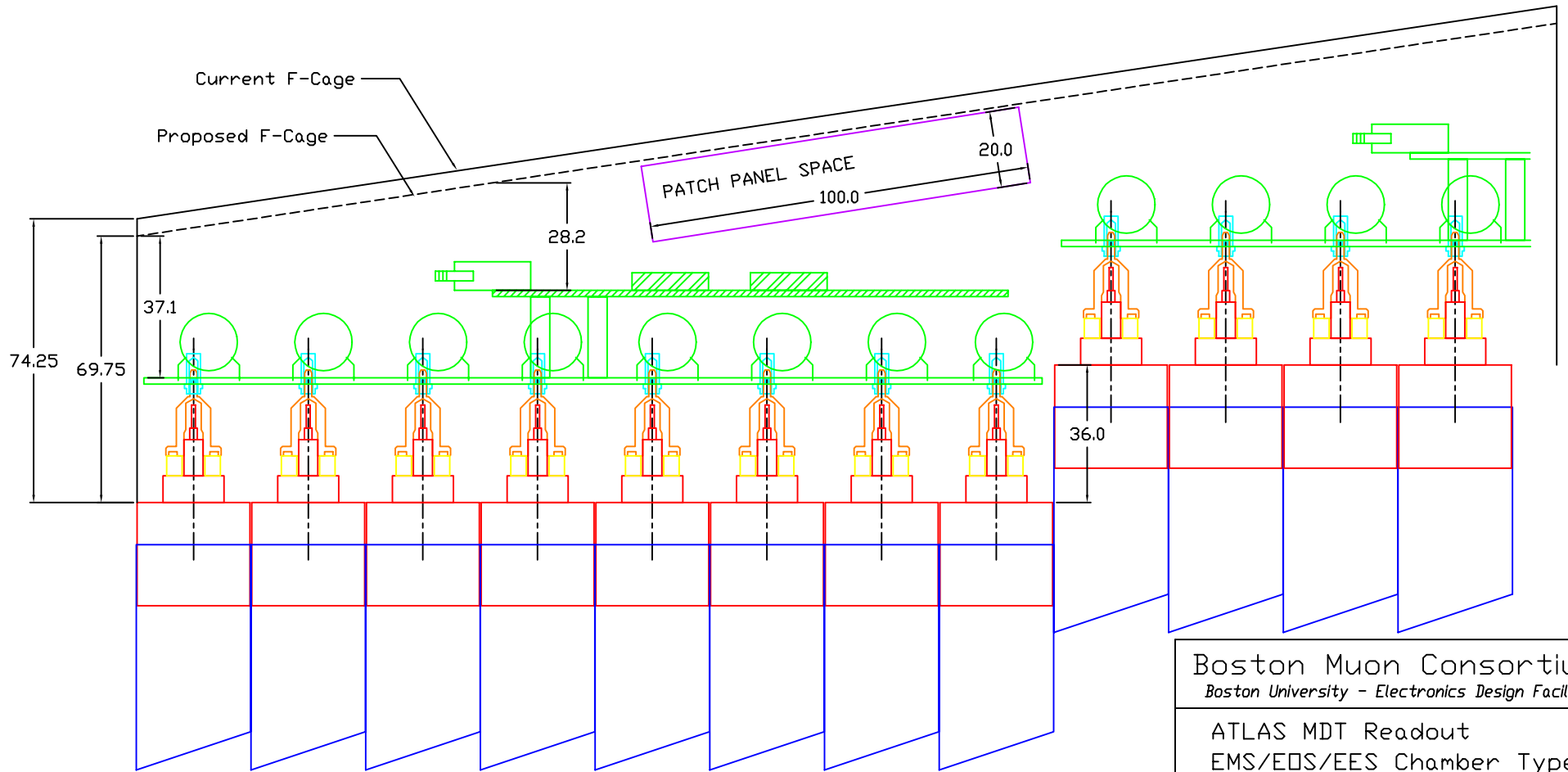
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ATLAS MDT Readout
EML/EOL/EEL Chamber Type
PCB Stack Reference dwg

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EMS/EOS/EES



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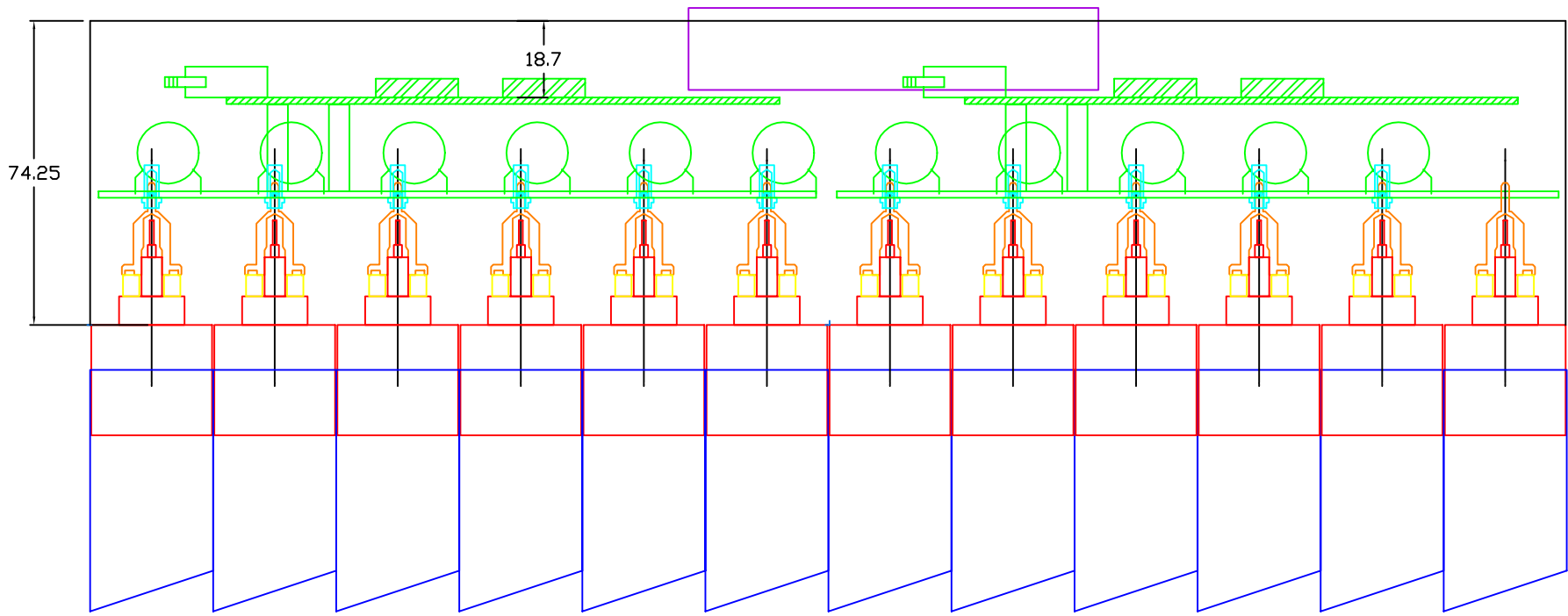
SCALE: *DO NOT SCALE*

Revision: XXX

Dwg: E. Hazen

Date: 1999-01-28

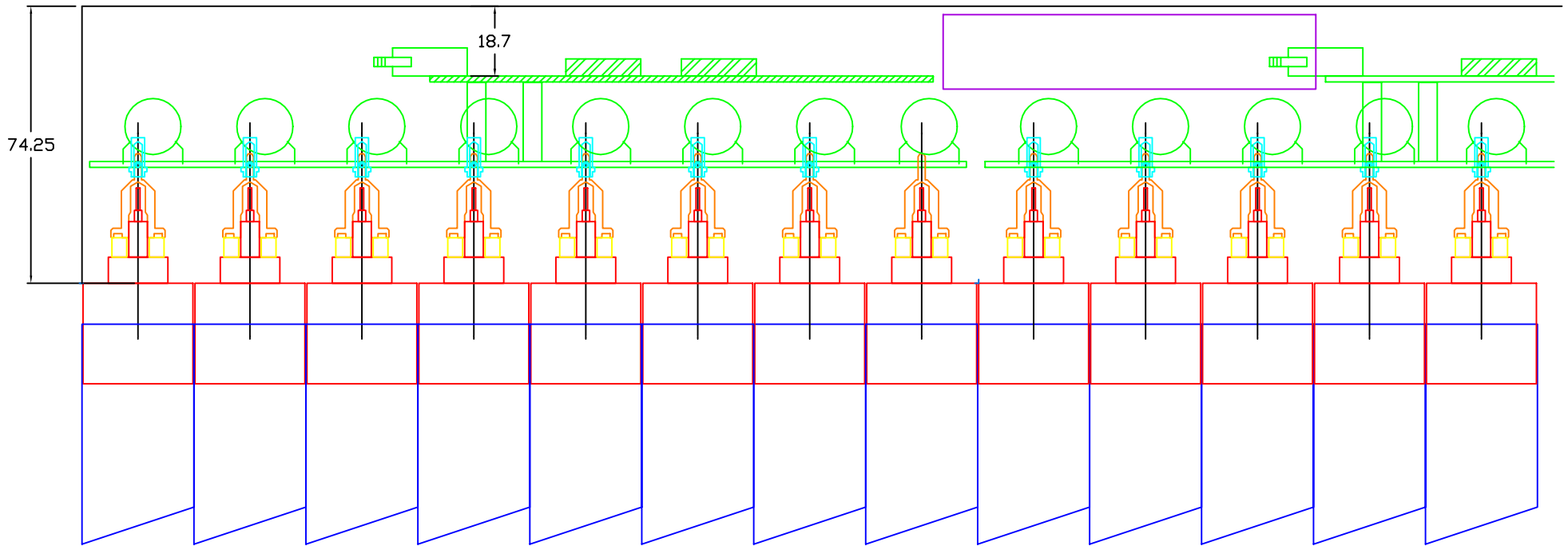
BI



NOTES: UPDATED to 1998-12-27 Endplug Drawings
TUBES MUST BE SHORTENED 15mm EACH END
TO CONFORM TO M4 ENVELOPE

| | |
|--|-------------------------|
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| ATLAS MDT Readout BI Chambers PCB Stack Reference | |
| SCALE: <i>DO NOT SCALE</i> | Revision: <i>XXX</i> |
| Dwg: <i>E. Hazen</i> | Date: <i>1999-12-14</i> |

BM/B0



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ATLAS MDT Readout
BM/B0 Chambers
PCB Stack Reference

SCALE: DO NOT SCALE Revision: XXX

Dwg: E. Hazen Date: 1999-12-14