

ATLAS MDT Mezzanine Lite Revision E - Connector Locations

Threshold Test
(left 2 pins)

Threshold Jumper
left 2: POT
right 2: DAC

Pin 1

Analog Power
1. +6V
2. AGND

JTAG out
to CSM

Discriminator output test points
(LVDS differential)
channels 1-8

channels 9-16

JTAG in
from CSM

Digital Power/
TDC Link
11. +6V
12. DGND

channels 17-24

