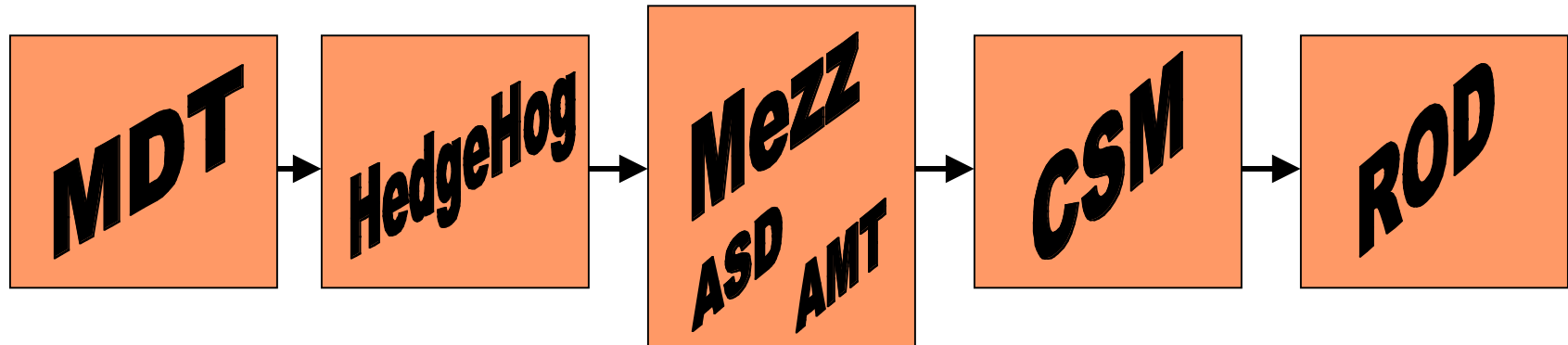
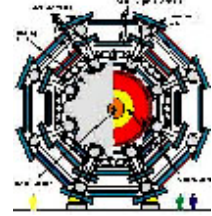


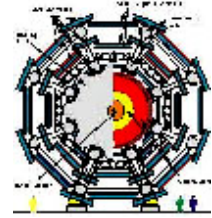


ATLAS MDT Electronics Responsibilities, Costs & Schedule





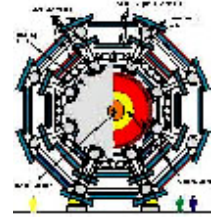
Design Responsibilities



- HedgeHog Boards
 - Barrel: Pavia (HV), Rome III (signal)
 - EndCap: MIT (HV), Boston U (signal)
- ASD Chip - Boston U and Harvard
- AMT TDC Chip - KEK
- Mezzanine Boards - Harvard and Boston U
- Chamber Service Module - Michigan
- ROD - NIKHEF



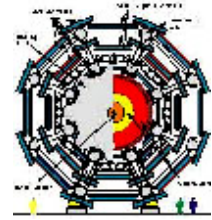
Production Responsibilities



- HedgeHogs (common production by CERN)
 - HV - INFN (all types)
 - Signal - Boston (EndCaps), Chamber-Builders (Barrel)
- ASD - Boston U and Harvard
- AMT-TDC - KEK
- Mezzanine Boards - Harvard and Boston U
- Chamber Service Module - **To Be Determined!**
- ROD - (Freiburg, Munich LMU, Munich MPI) 30%,
Nijmegen 41%, Saclay 33%, JINR 6%



Production Costs



- HedgeHogs (32,000 HV+signal): 1,960 kCHF
 - (assumes common CERN production)
- ASD (45,000): \$375K \approx 600 kCHF
 - (source: recent HP quote)
- AMT-TDC (16,000): 1,415 kCHF
 - (source: ATLAS MOU)
- Mezz Boards (16,000): \$1,875K \approx 3,000 kCHF
 - (assumes US production – European prod. being investigated)
- CSM (1,320): \$1,221K \approx 1,950 kCHF
 - (funding source under negotiation)



Schedule

